

20

FIG. 1 is a block diagram of a system 20 for a DSL modem 28. The system 20 includes a CPU 30, a memory 32, a modem driver 34, an initialization module 36, a tables 38, a buffer 40, and a MNT 42. The CPU 30 is connected to the memory 32, the modem driver 34, the initialization module 36, the tables 38, the buffer 40, and the MNT 42. The modem driver 34 is connected to the initialization module 36, the tables 38, the buffer 40, and the MNT 42. The initialization module 36 is connected to the tables 38, the buffer 40, and the MNT 42. The tables 38 are connected to the buffer 40 and the MNT 42. The buffer 40 is connected to the MNT 42. The MNT 42 is connected to the CPU 30. The system 20 is connected to a DSL modem 28 via a transmission line 32. The DSL modem 28 is connected to a remote terminal 26 via a transmission line 32. The DSL modem 28 is also connected to a core network 22 via a transmission line 32. The core network 22 is connected to a central office 24 via a transmission line 32. The central office 24 is connected to a DSL modem 26 via a transmission line 32. The DSL modem 26 is connected to a CPU 34, a memory 38, a modem driver 40, an initialization module 42, a tables 44, a buffer 46, and a MNT 48. The CPU 34 is connected to the memory 38, the modem driver 40, the initialization module 42, the tables 44, the buffer 46, and the MNT 48. The modem driver 40 is connected to the initialization module 42, the tables 44, the buffer 46, and the MNT 48. The initialization module 42 is connected to the tables 44, the buffer 46, and the MNT 48. The tables 44 are connected to the buffer 46 and the MNT 48. The buffer 46 is connected to the MNT 48. The MNT 48 is connected to the CPU 34. The system 20 is connected to a DSL modem 28 via a transmission line 32. The DSL modem 28 is connected to a remote terminal 26 via a transmission line 32. The DSL modem 28 is also connected to a core network 22 via a transmission line 32. The core network 22 is connected to a central office 24 via a transmission line 32. The central office 24 is connected to a DSL modem 26 via a transmission line 32. The DSL modem 26 is connected to a CPU 34, a memory 38, a modem driver 40, an initialization module 42, a tables 44, a buffer 46, and a MNT 48. The CPU 34 is connected to the memory 38, the modem driver 40, the initialization module 42, the tables 44, the buffer 46, and the MNT 48. The modem driver 40 is connected to the initialization module 42, the tables 44, the buffer 46, and the MNT 48. The initialization module 42 is connected to the tables 44, the buffer 46, and the MNT 48. The tables 44 are connected to the buffer 46 and the MNT 48. The buffer 46 is connected to the MNT 48. The MNT 48 is connected to the CPU 34.

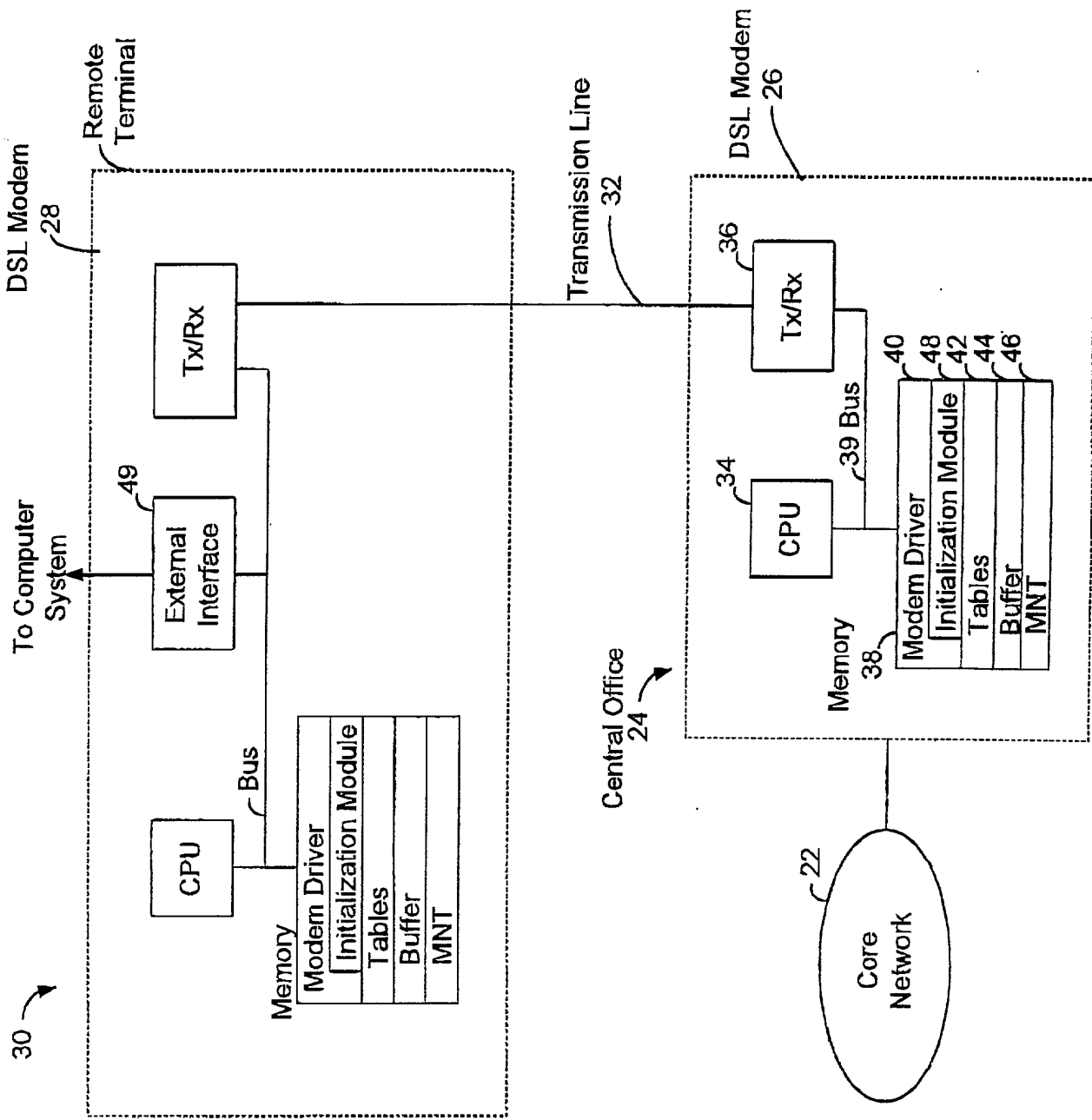


FIG. 1

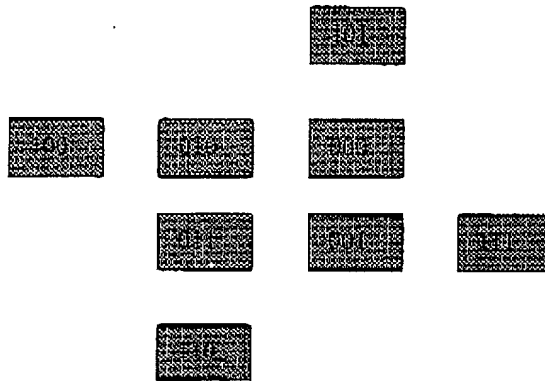


FIG. 2

$1/\omega(b)$

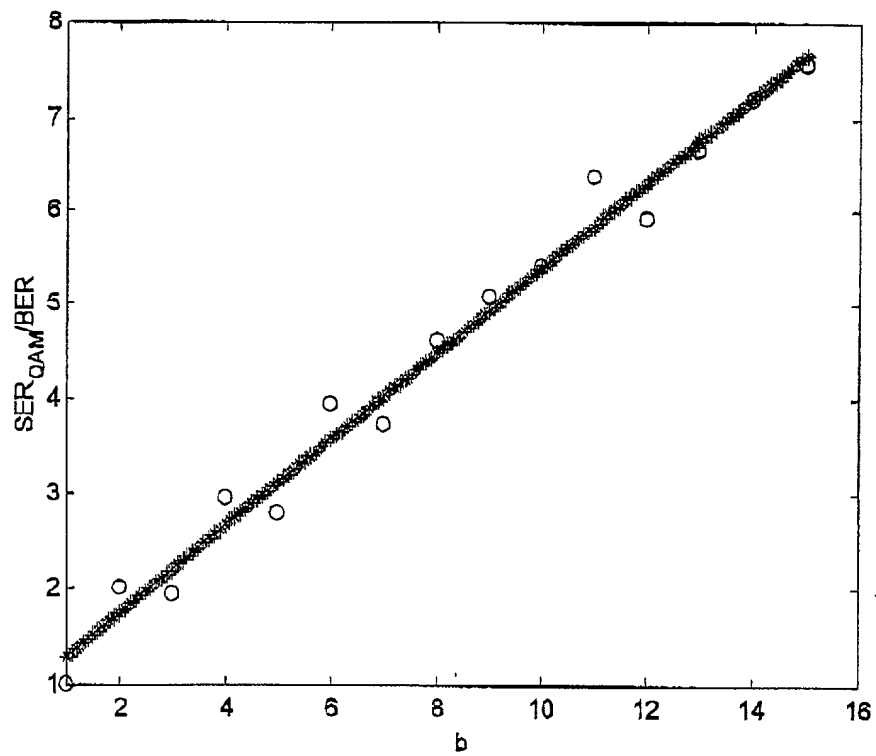


FIG. 3

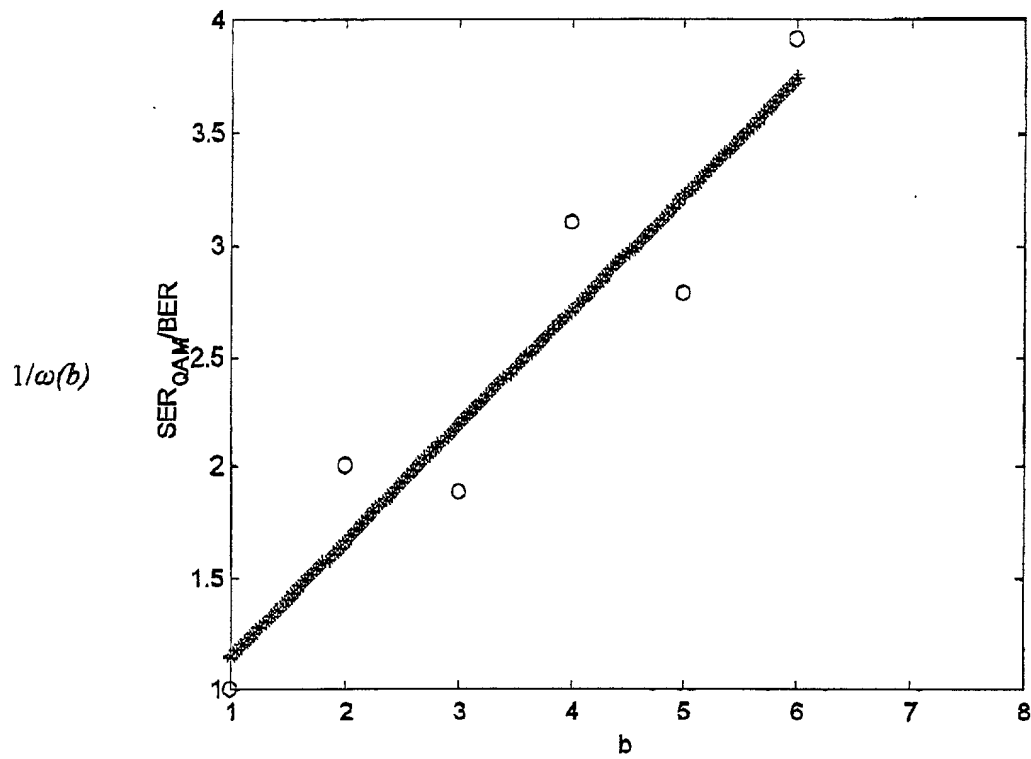


FIG. 4

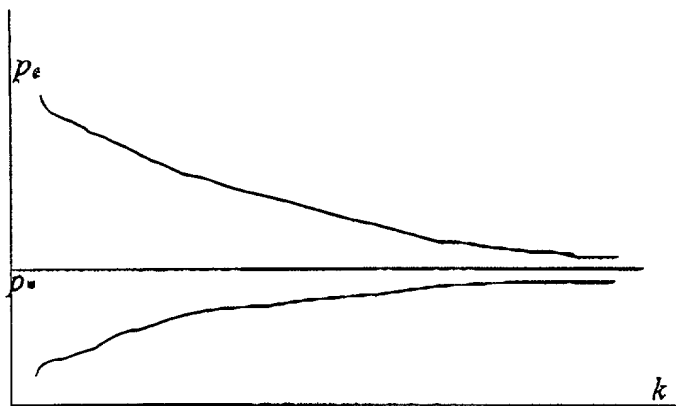


FIG. 6

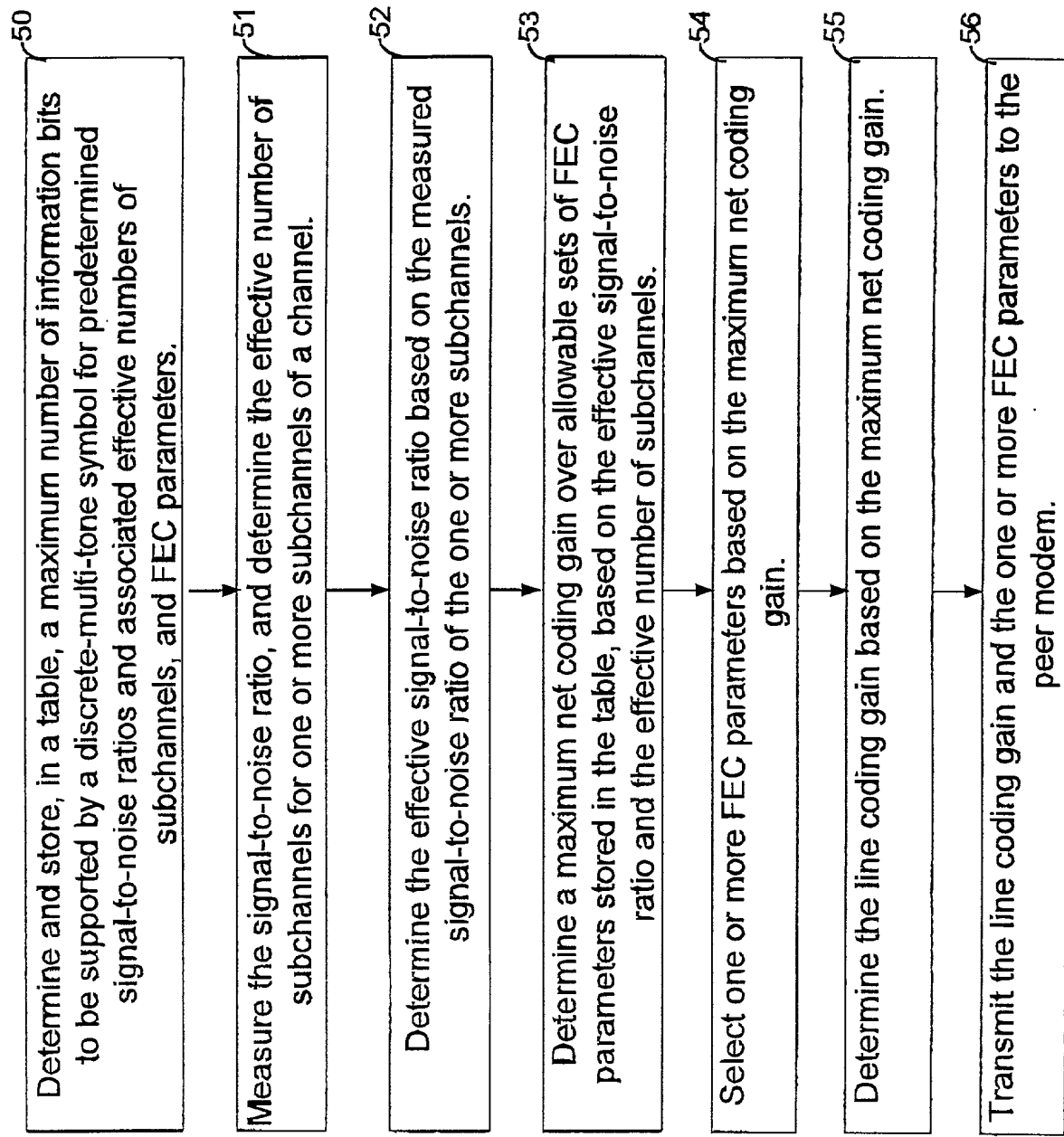


FIG. 5

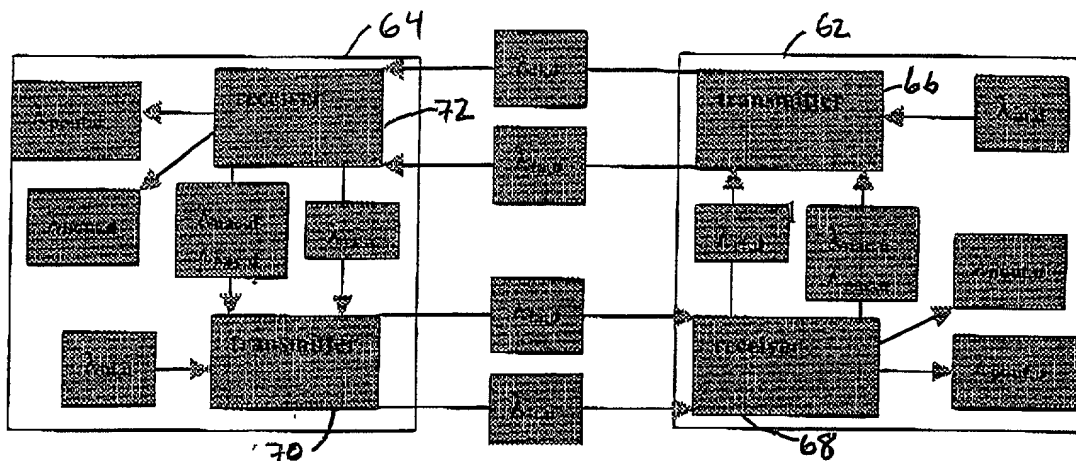


FIG. 7A

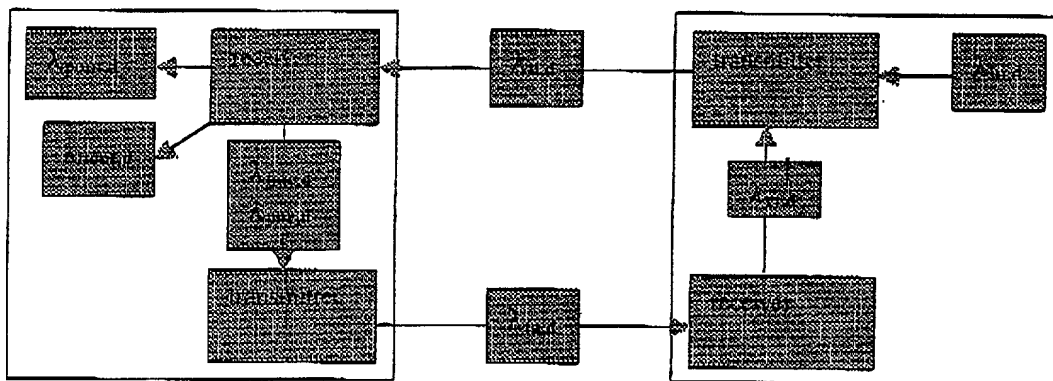


FIG. 7B

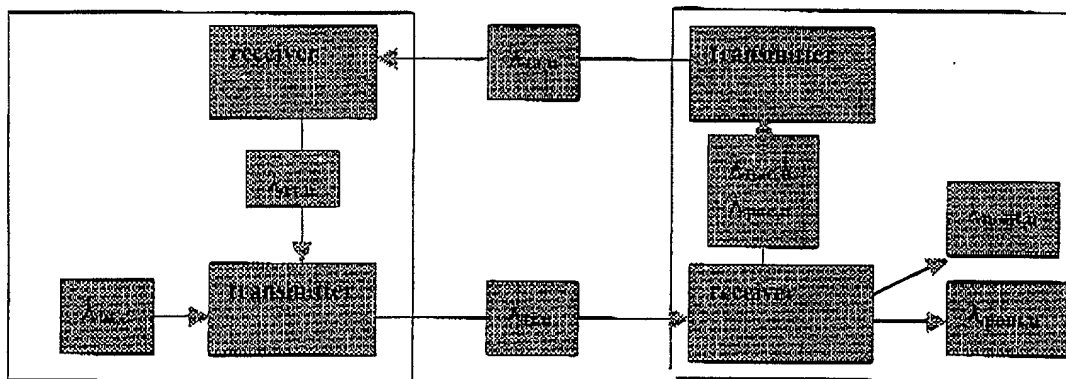


FIG. 7C

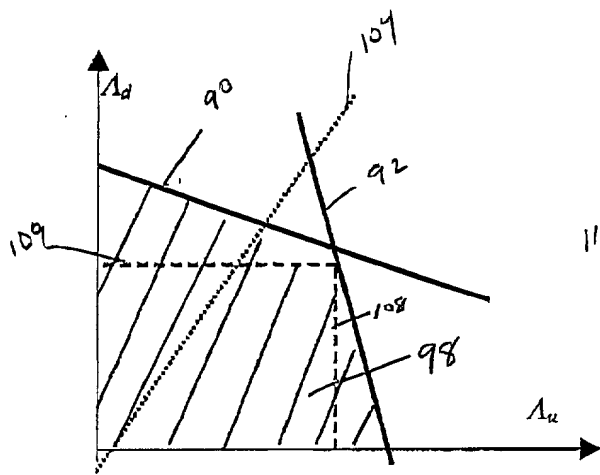


FIG. 8A

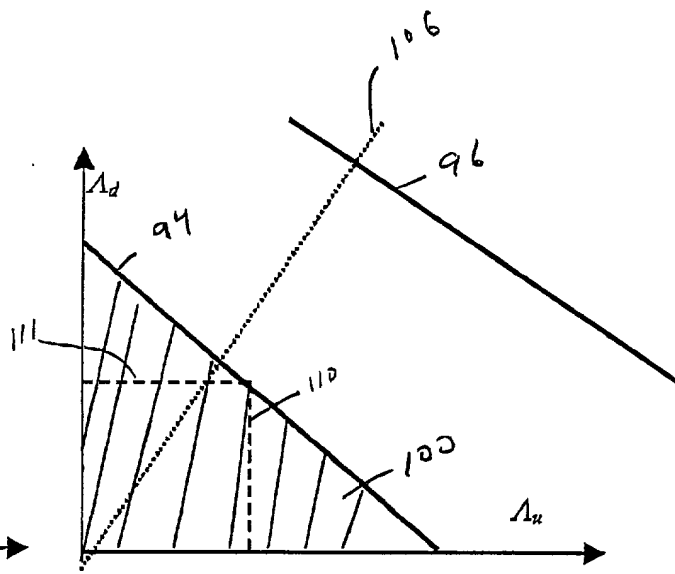


FIG. 8B

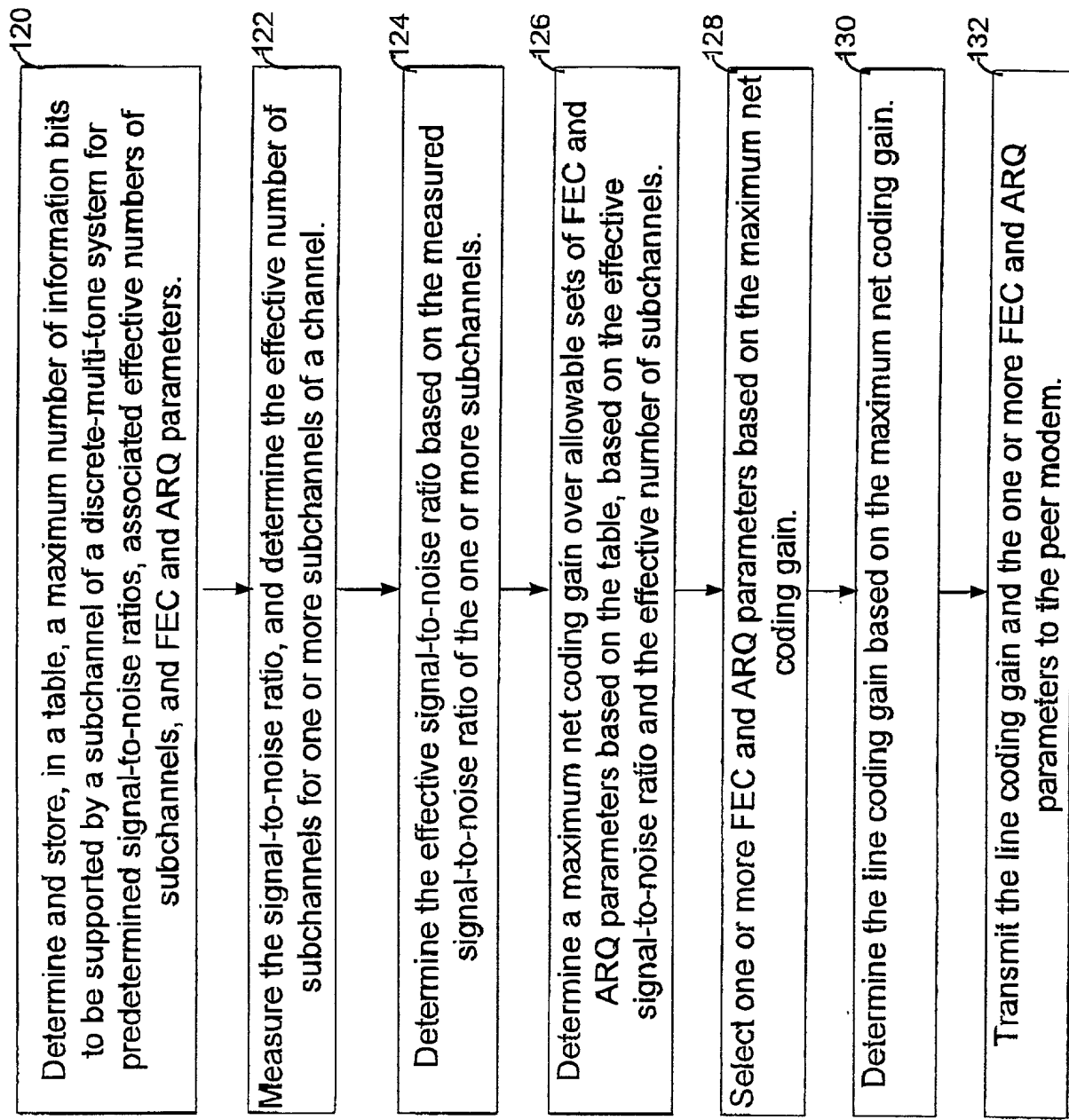


FIG. 9

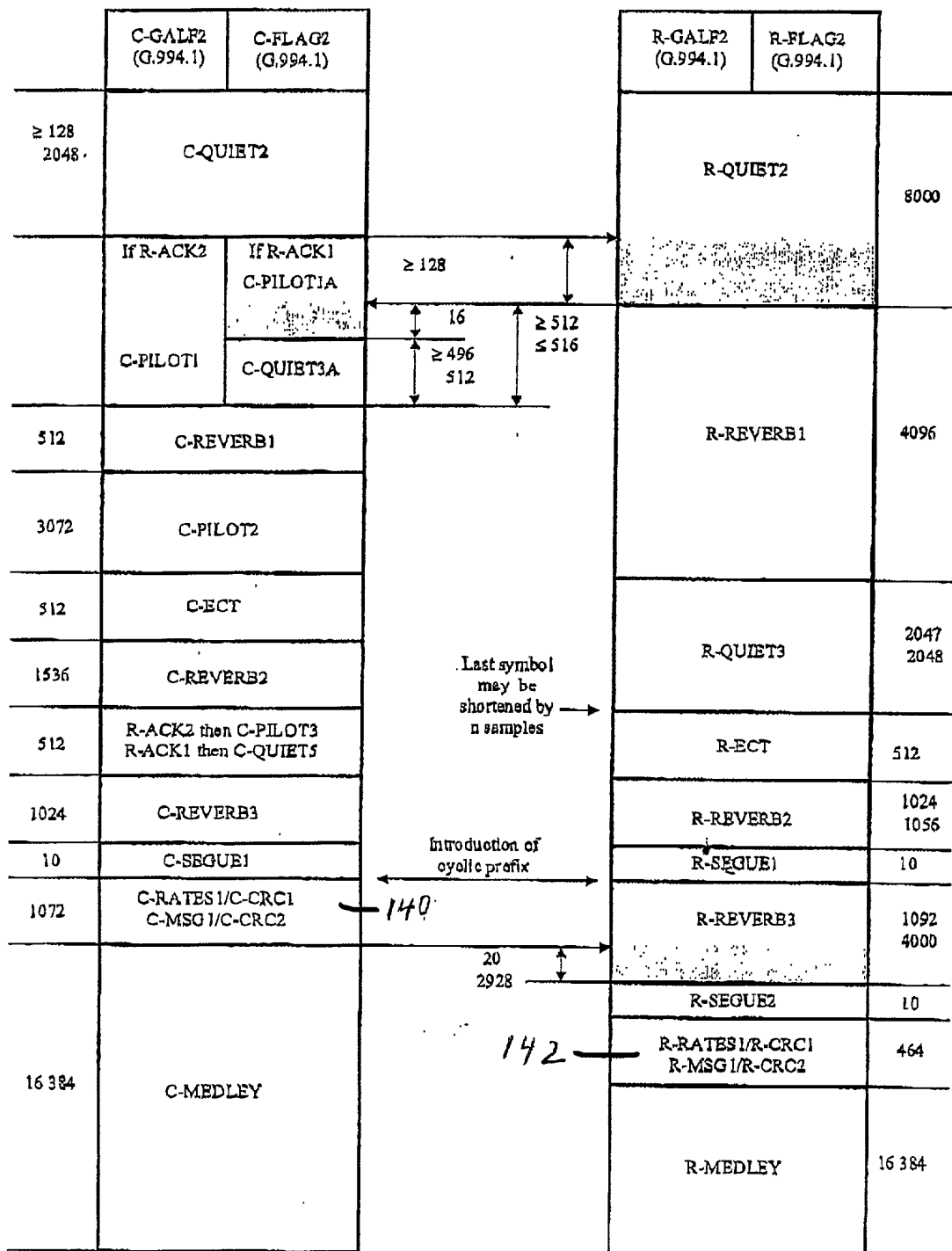
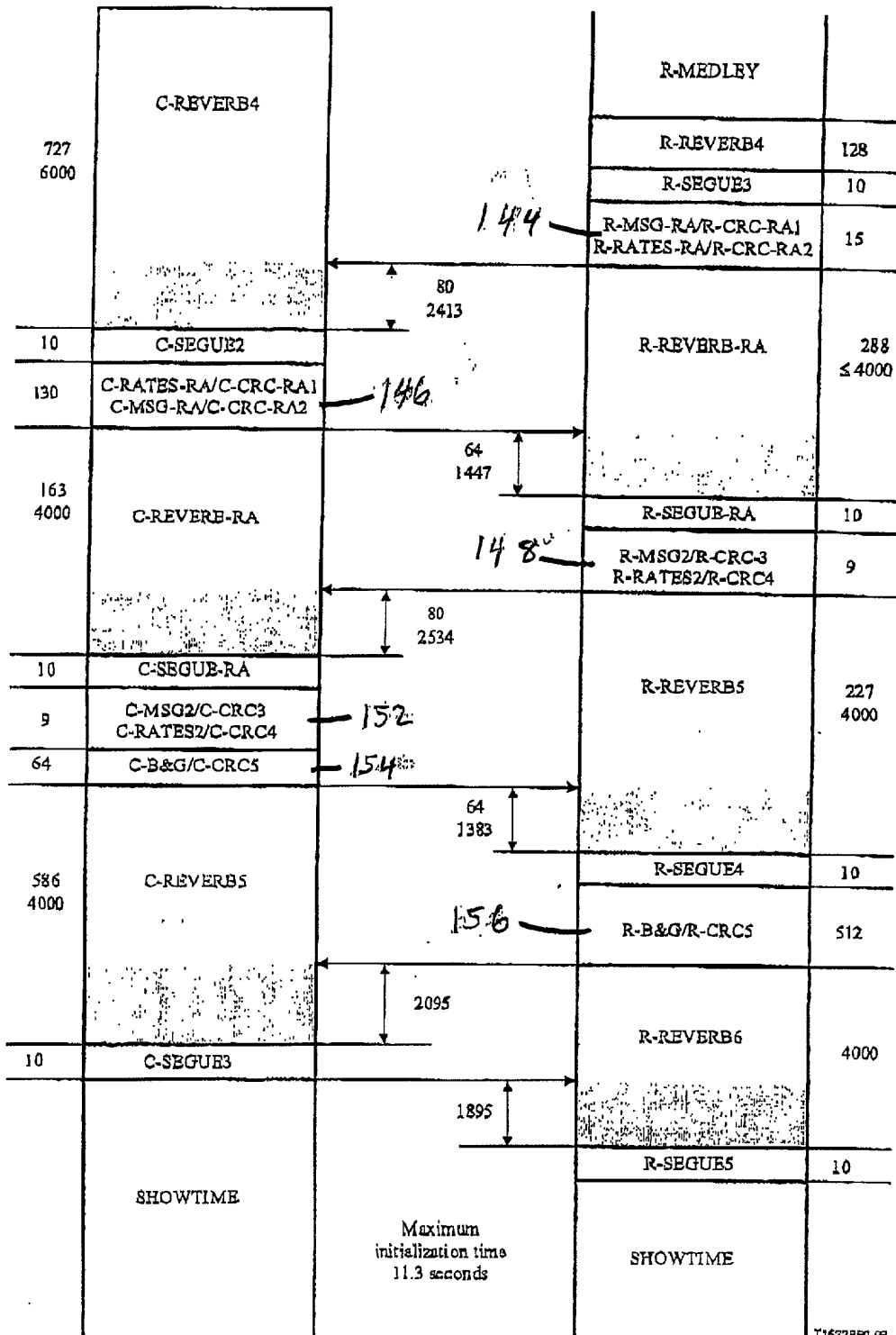


Fig. 10A



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Fig. 10B